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CONFIRMATION NO. 8350

<b>SERIAL NUMBER</b> 10/550,652	<b>FILING OR 371(c) DATE</b> 12/07/2005 <b>RULE</b>	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2811	<b>ATTORNEY DOCKET NO.</b> 125426	
<b>APPLICANTS</b> Satoshi Sugahara, Kanagawa, JAPAN; Tomohiro Matsuno, Tokyo, JAPAN; Masaaki Tanaka, Saitama, JAPAN;					
<b>** CONTINUING DATA *****</b> This application is a 371 of PCT/JP04/04379 03/26/2004					
<b>** FOREIGN APPLICATIONS *****</b> JAPAN 2003086499 03/26/2003					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 10	<b>TOTAL CLAIMS</b> 45	<b>INDEPENDENT CLAIMS</b> 5
<b>ADDRESS</b> 25944					
<b>TITLE</b> Reconfigurable logical circuit using transistor having spi-dependent transmission characteristics					
<b>FILING FEE RECEIVED</b> 2930	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		